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PTO/SB/08a (05-03)
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U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Attorney Docket Number

03 P 52507 US (90035.211)

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

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Complete if Known

Application Number

Filled Herewith

First Named Inventor

V. C. JaiPrakash

Art Unit

Examiner Name

TBA

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of

			U.S. PAT	ENT DOCUMENTS	
Examiner Initials	Cite No. <sup>1</sup>	Document Number  2 Number Kind Code (If known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
hob	1	US- 5119153	06-02-1992	Korman, et al.	
101-	2	US- 5225697	07-06-1993	Malhi, et al.	
~	3	US- 6153902	11-28-2000	Furukawa, et al.	
<b>10</b> ~	4	US- 6281539	08-28-2001	Mandelman, et al.	
12	5	US- 6333533	12-25-2001	Furukawa, et al.	
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		,		FORE	IGN PATENT DO	CUMENTS		
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		Office <sup>3</sup>	Number*	Kind <sup>6</sup> (if known)	MM-DD-YYYY	Applicant of Cited Document	Passage or Palmont	T <sup>0</sup>
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Substitute for form 1449B/PTO			Co	Complete if Known		
			Application Number	TBK 10/6/15 11		
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STATEMENT BY APPLICANT			T First Named Inventor	V. C. JaiPrakash		
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Sheet	2	of g	Attorney Docket Number	03 P 52507 US (90035.211)		

		OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T2
LOR	6	Article entitled "A Novel Trench DRAM Cell with a VERTICAL Access Transistor And BuriEd STrap (VERI BEST) for 4 Gb/16Gb by U. Gruening et al., IEDM (IEEE), 1999.	
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